

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of ) Art Unit: 2822  
Shunpei YAMAZAKI et al. ) Examiner: M. Wilczewski  
Serial No. 08/691,434 )  
Filed: August 2, 1996 )  
For: METHOD OF FABRICATING )  
SEMICONDUCTOR DEVICES AND )  
APPARATUS FOR PROCESSING A )  
SEMICONDUCTOR )

**CERTIFICATE OF MAILING**

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# **INFORMATION DISCLOSURE STATEMENT BY APPLICANT**

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## **Complete if Known**

Application Number	08/691,434
Filing Date	August 2, 1996
First Named Inventor	Shunpei YAMAZAKI et al.
Group Art Unit	2822
Examiner Name	M. Wilczewski
Attorney Docket Number	0756-1551

Sheet 1 of 1

## **U.S. PATENT DOCUMENTS**

Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number	Kind Code <sup>2</sup> (if known)			
		6,566,175		Yamazaki et al.	05/20/2003	
		6,482,752		Yamazaki et al.	11/19/2002	
		6,261,877		Yamazaki et al.	07/17/2001	
		6,177,302		Yamazaki et al.	01/23/2001	

## **FOREIGN PATENT DOCUMENTS**

Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Foreign Patent Document			Name of Patentee or Applicant of Cited Document	Date of Publication of Cited Document MM-DD-YYYY	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T <sup>4</sup>
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## **OTHER PRIOR ART – NON PATENT LITERATURE DOCUMENTS**

Examiner Initials <sup>*</sup>	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>

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